

00110000 10110000

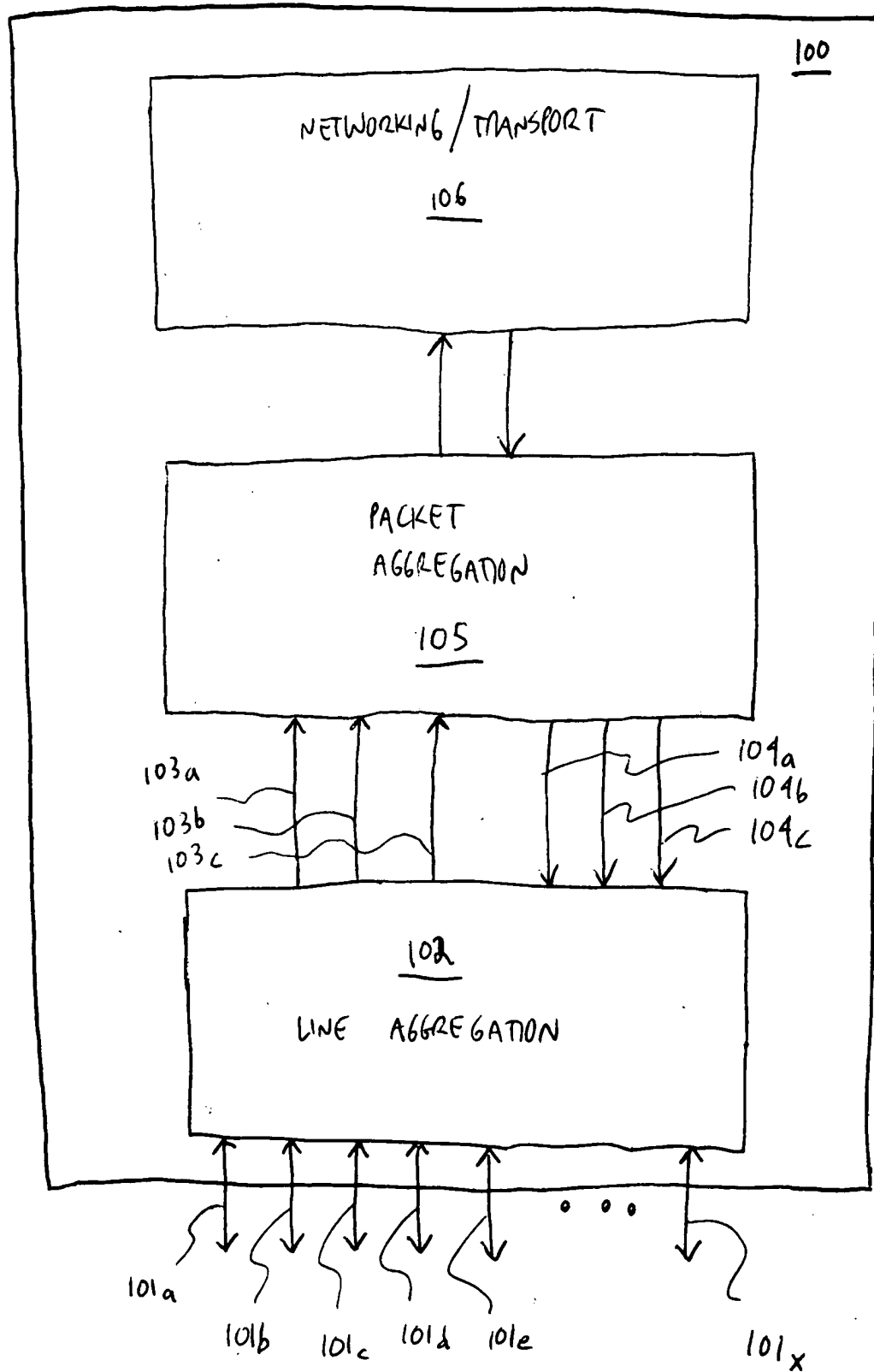


FIGURE 1 (PRIOR ART)

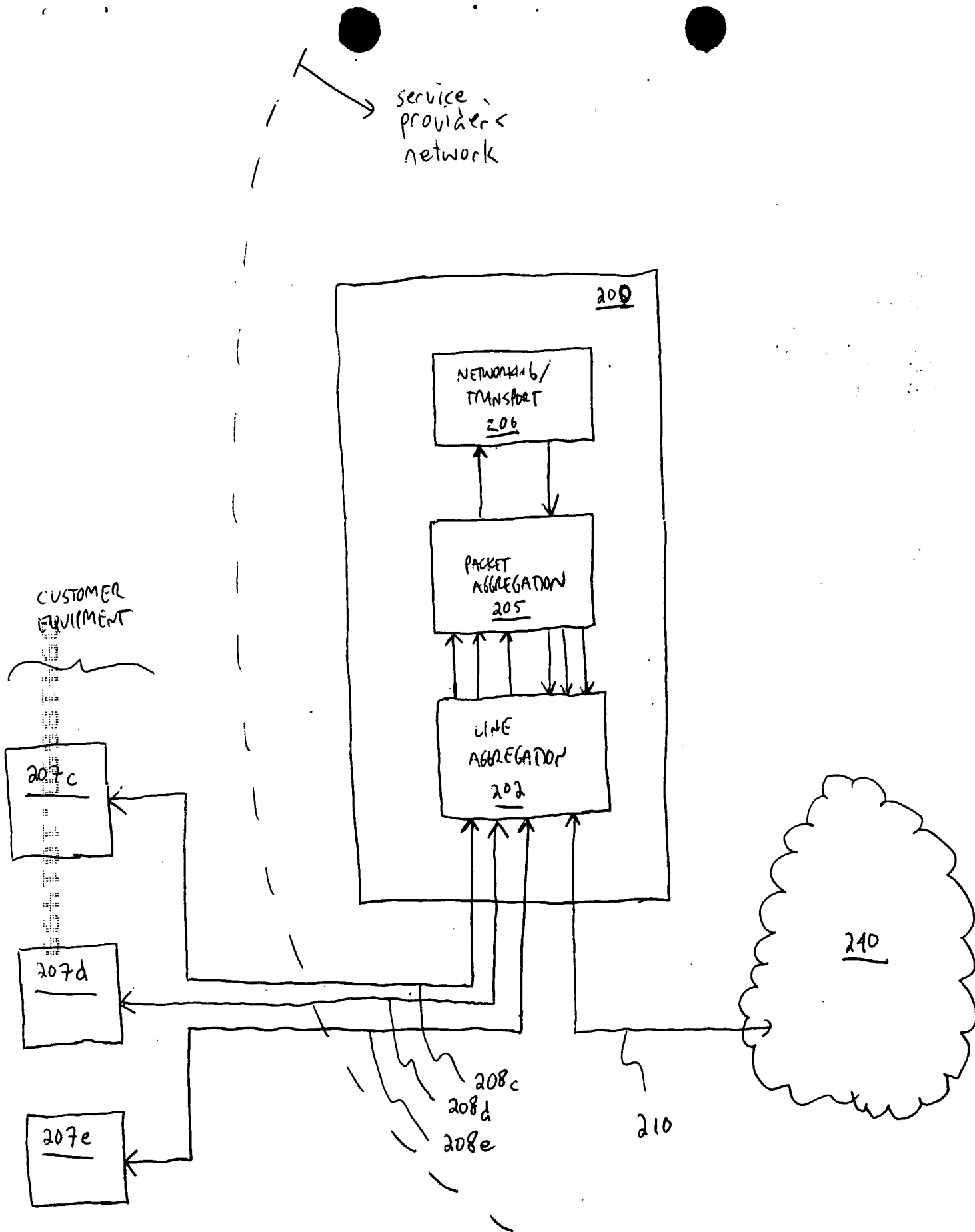


FIGURE 2A

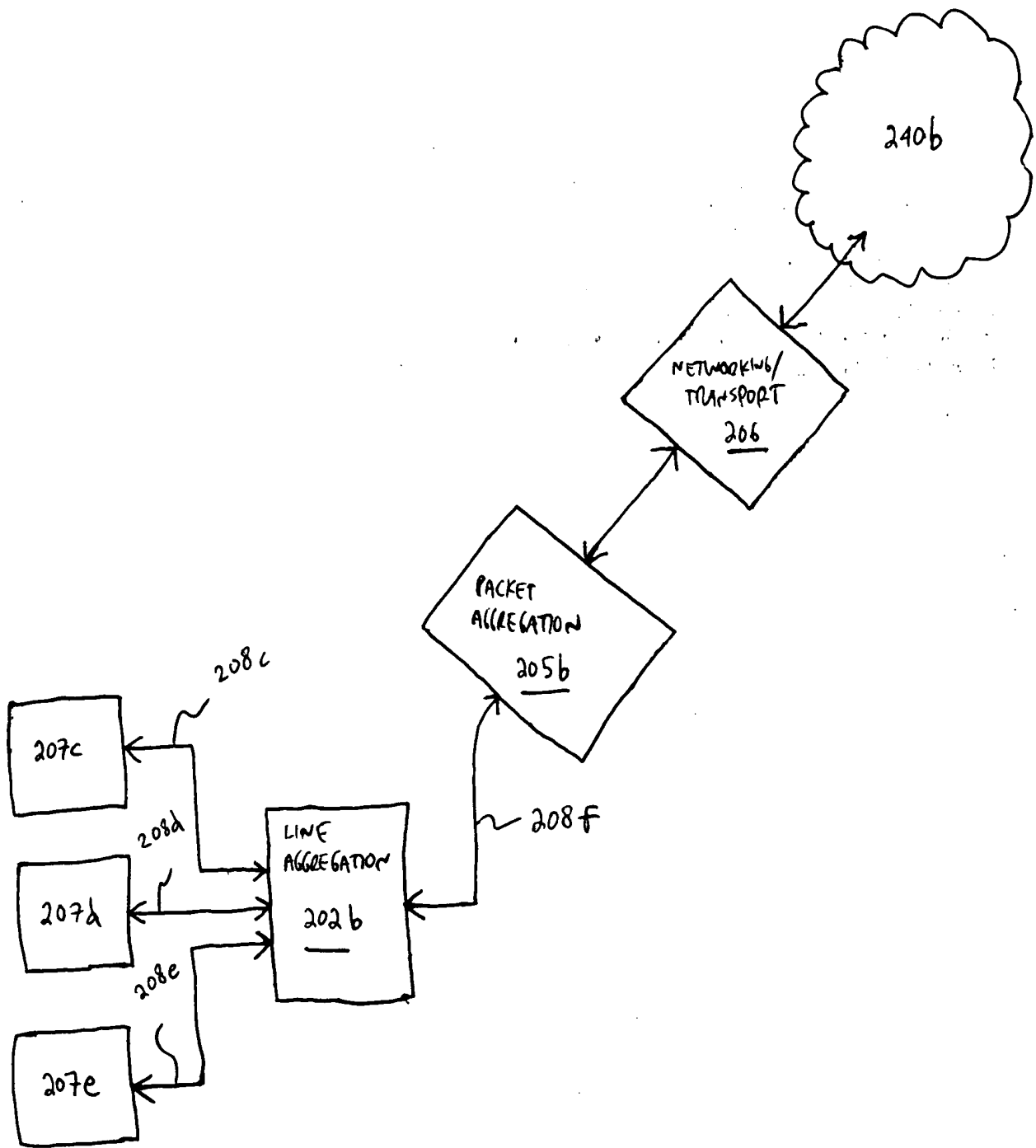


FIGURE 2b

00110000101100

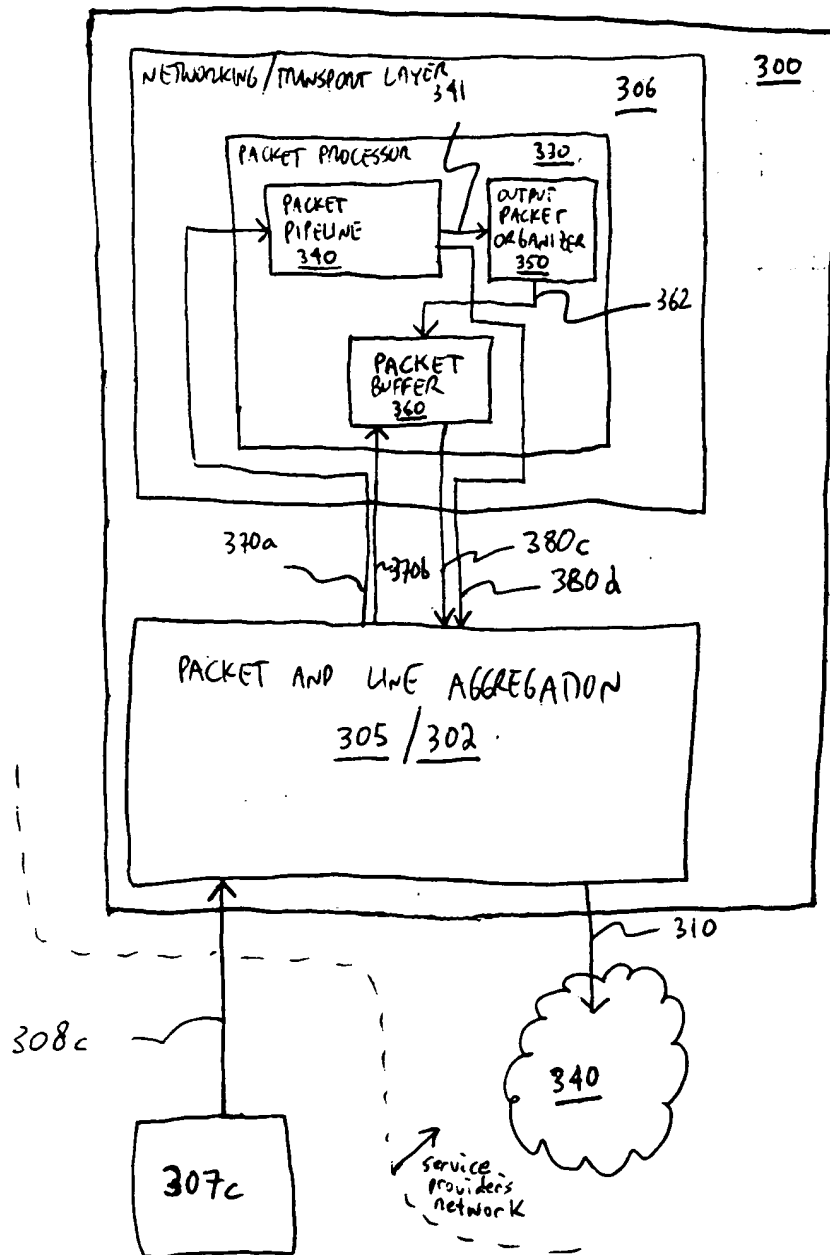


FIGURE 3A

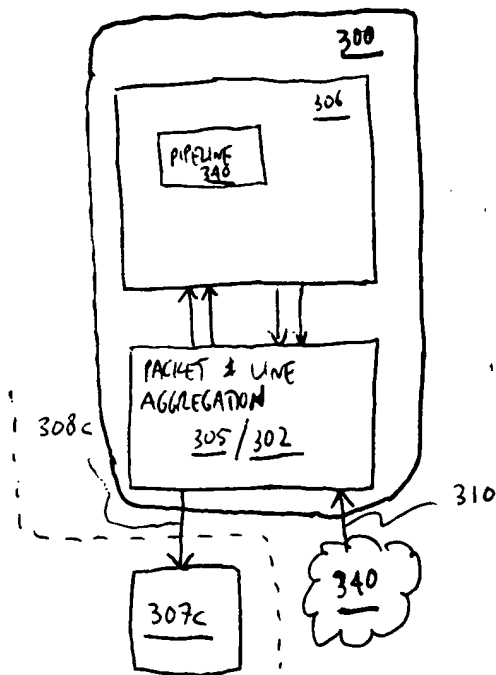


FIGURE 3B

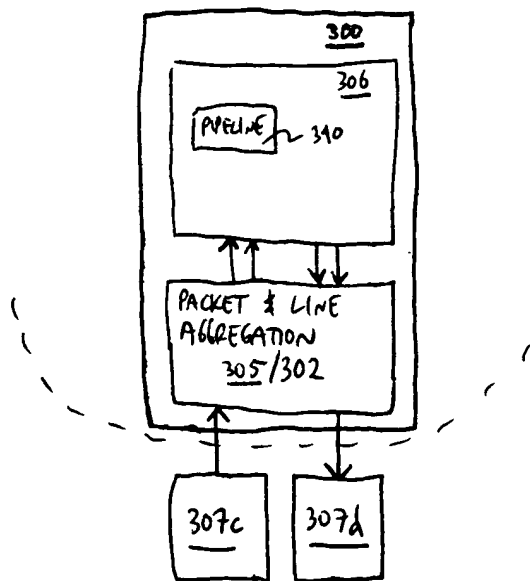
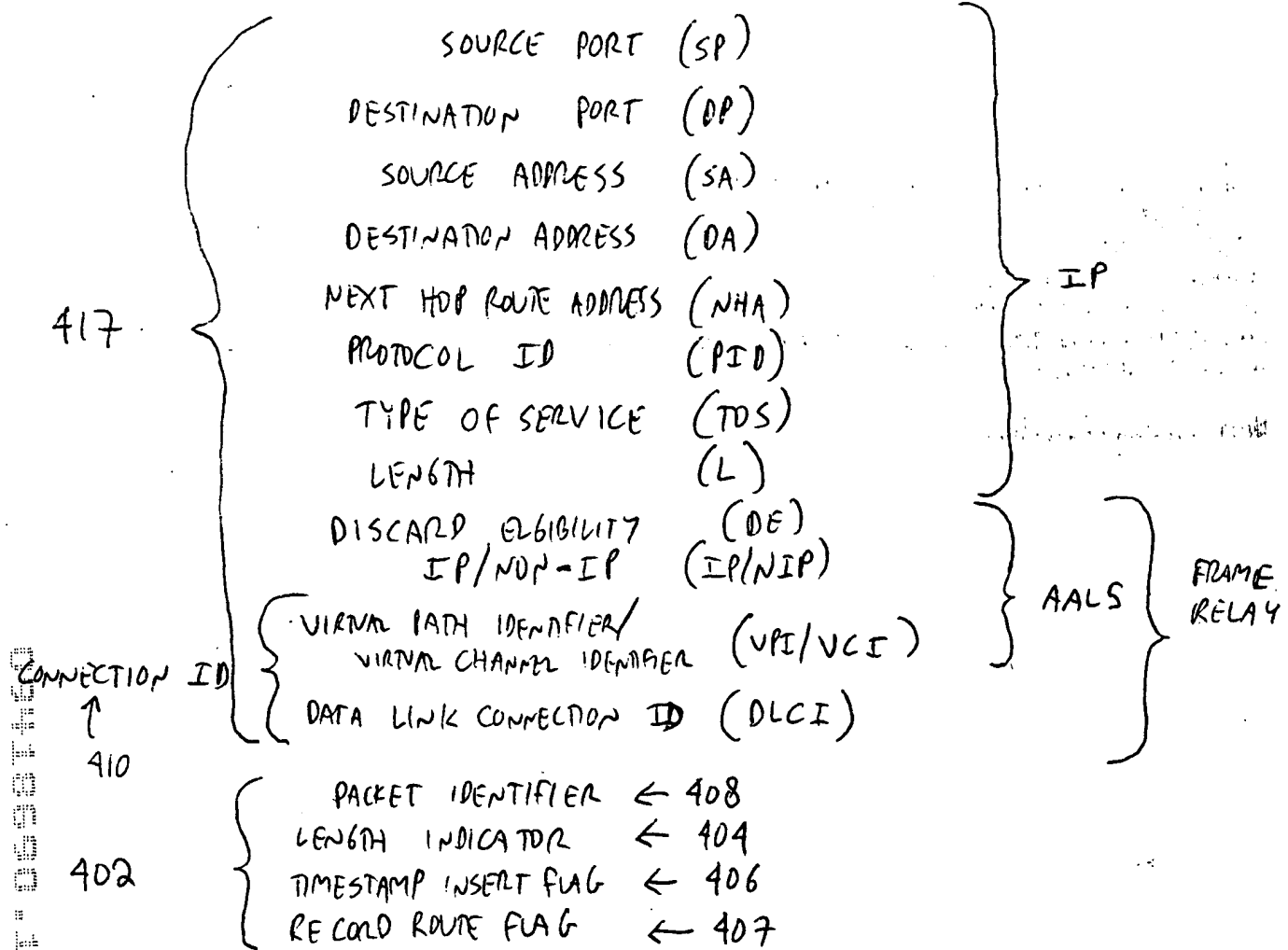


FIGURE 3C



495

FIGURE 4

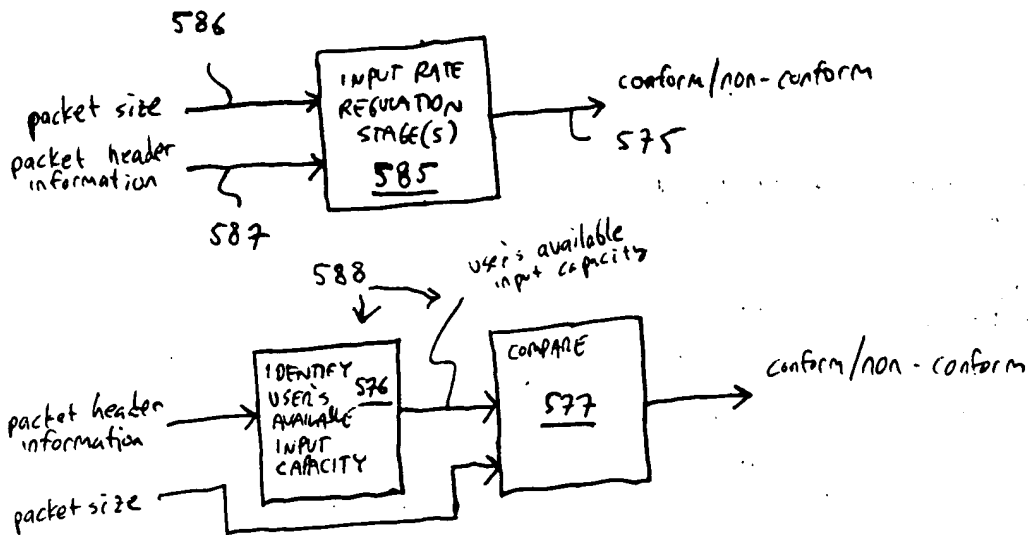


FIG. 5A

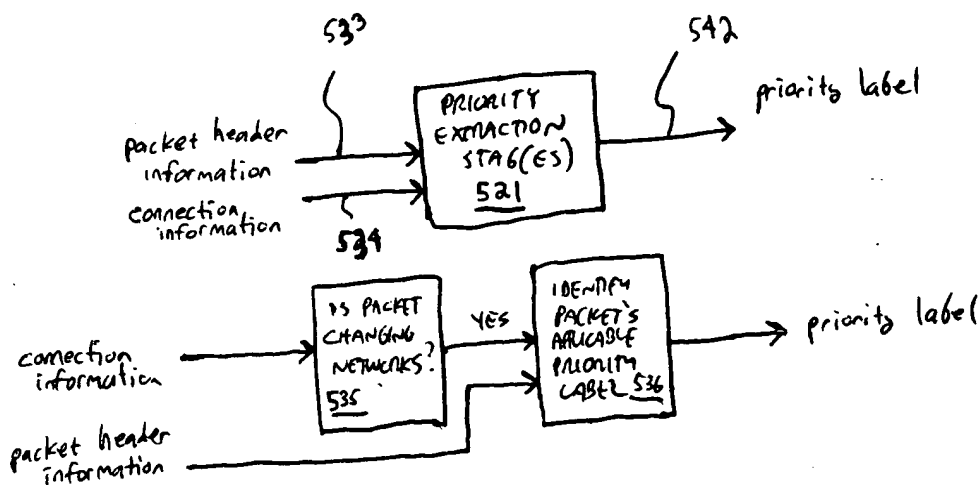


FIG. 5B

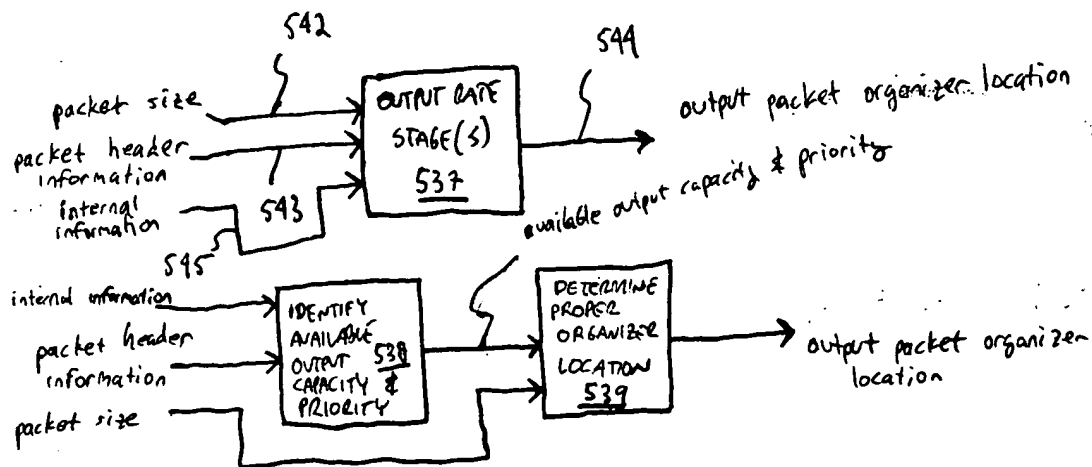


FIG. 5C

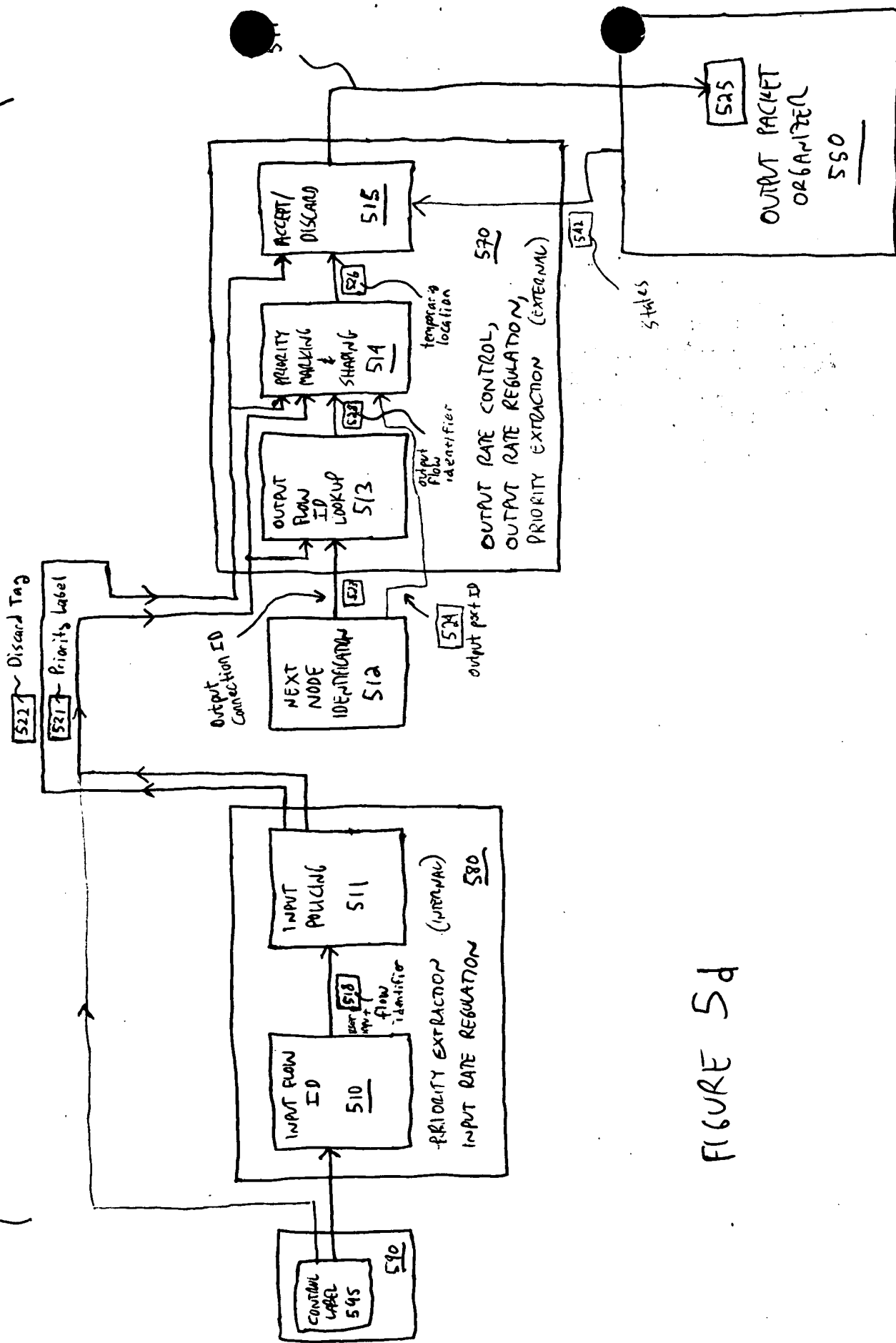


FIGURE 5d

SECTOR 05001160

	INPUT FLOW ID STAGE	INPUT POLICING STAGE	NEXT NODE IDENTIFICATION STAGE	OUTPUT FLOW ID STAGE	PRIORITY MARKING AND SHARING STAGE	ACCEPT/DISCARD STAGE
--	------------------------	-------------------------	--------------------------------------	-------------------------	--	-------------------------

FIG. 6A

X

X

X

X

X

X

FIG. 6B

X

X

X

X

FIG. 6C

X

X

X

X

X

X

FIG. 6D

X

X

X

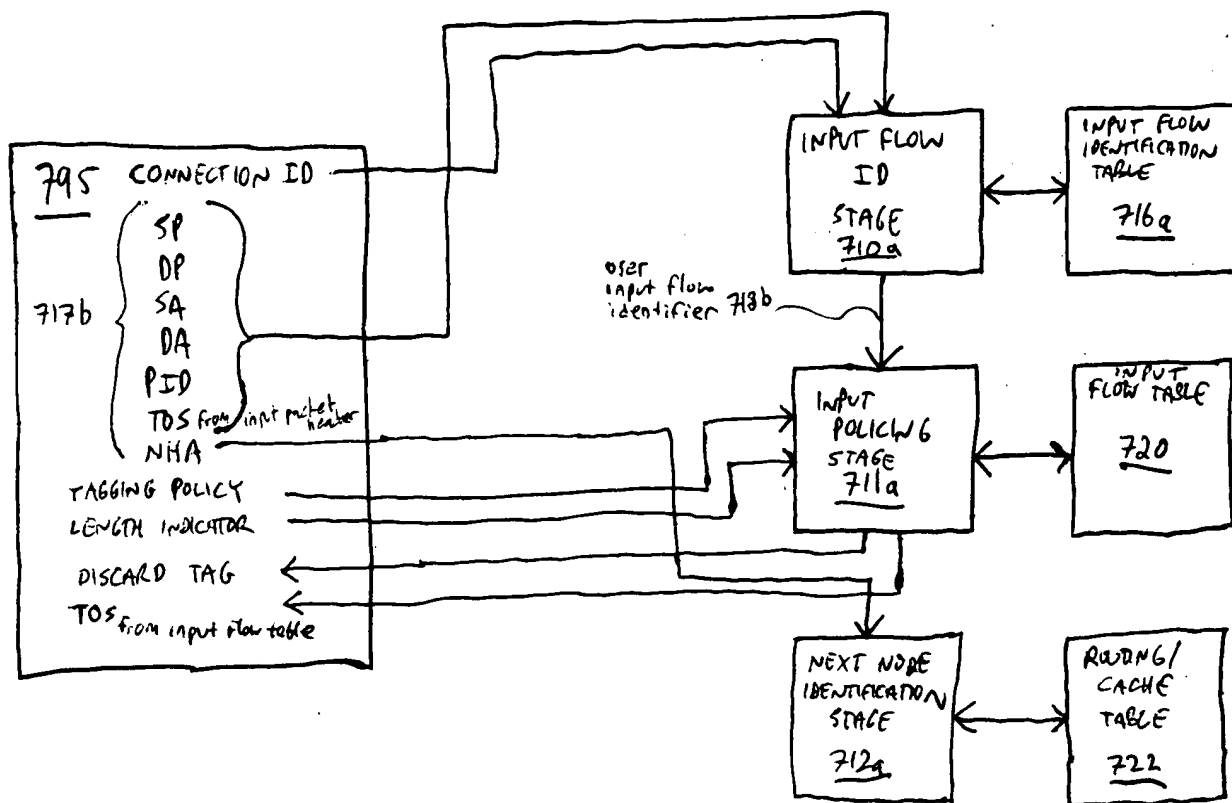


FIGURE 7A

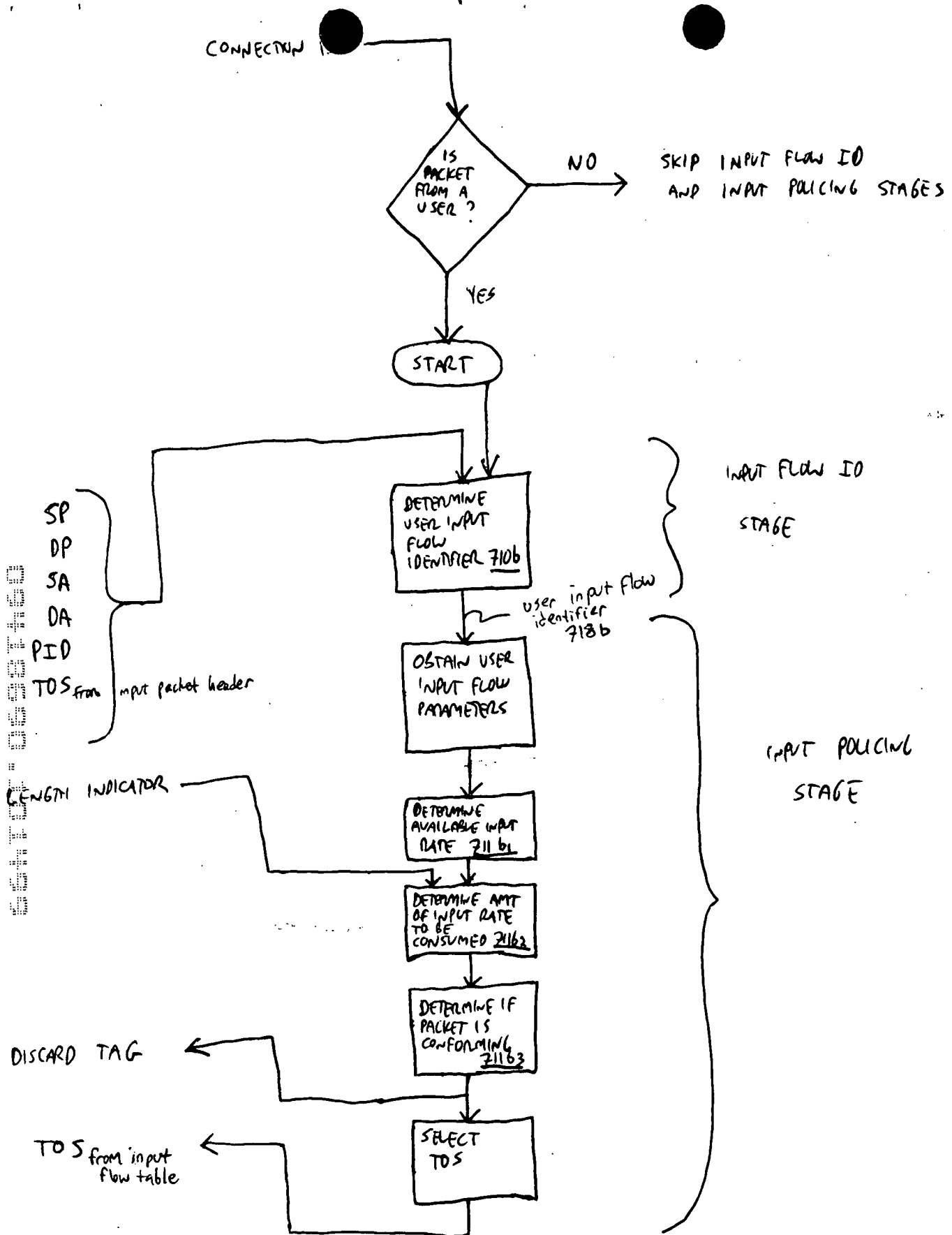


FIGURE 7B

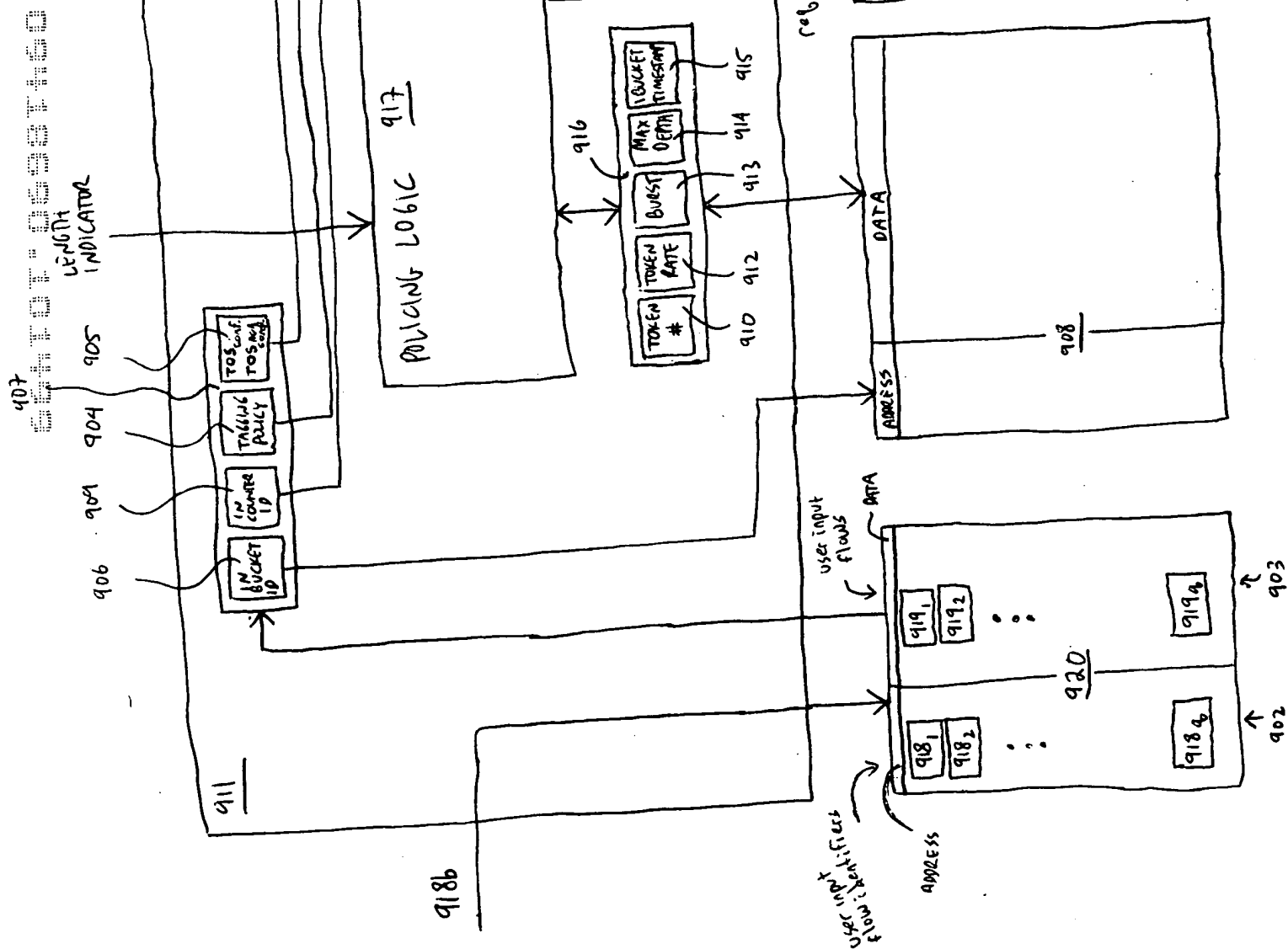
816

802

header information combinations		user input flow identifiers
817b ₁		818b ₁
817b ₂		818b ₂
817b ₃		818b ₃
817b ₄		818b ₄
817b ₅		818b ₅
		⋮
817b _m		818b _m
		⋮
817b _q		818b _q

FIGURE 8

FIGURE 9



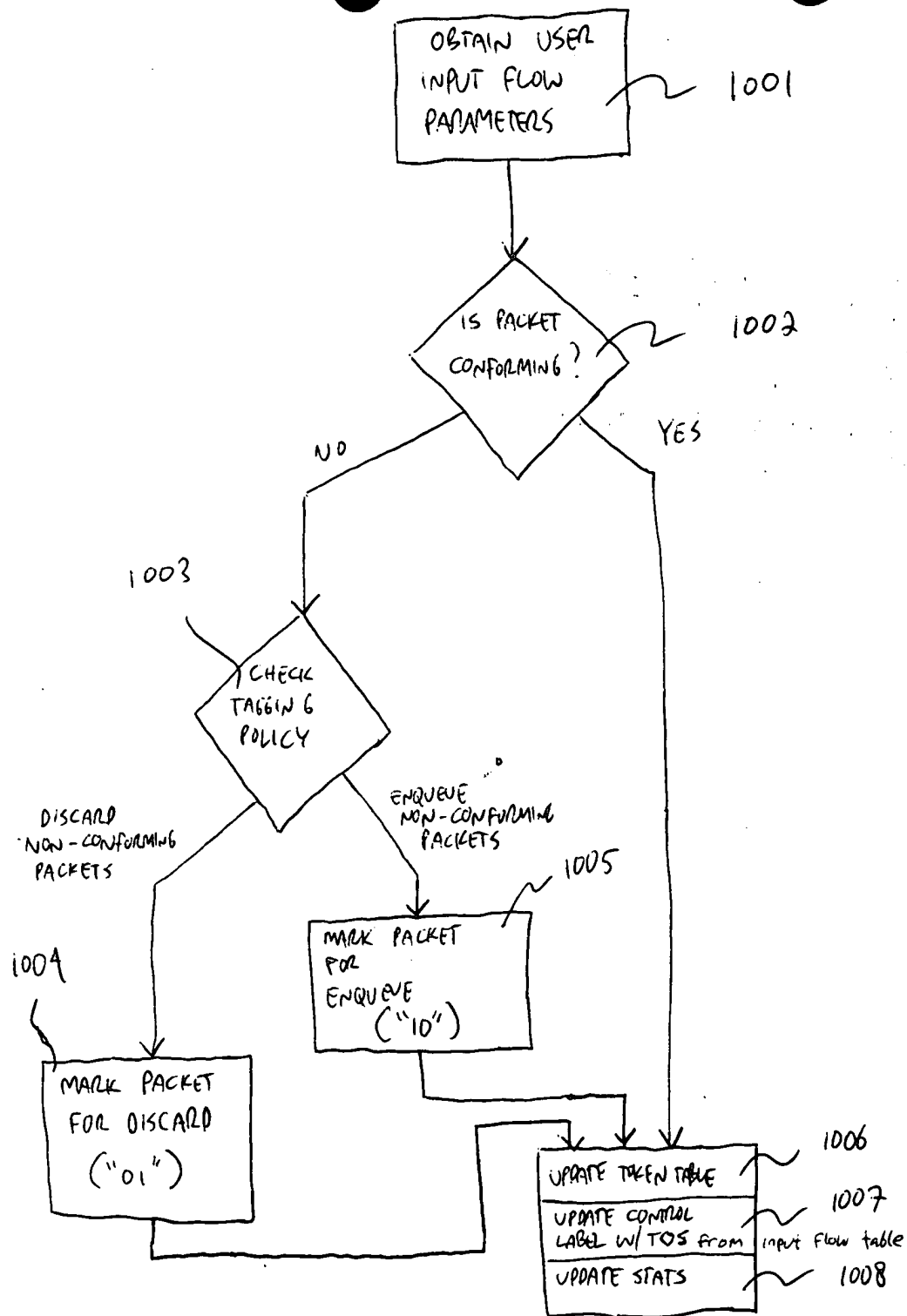
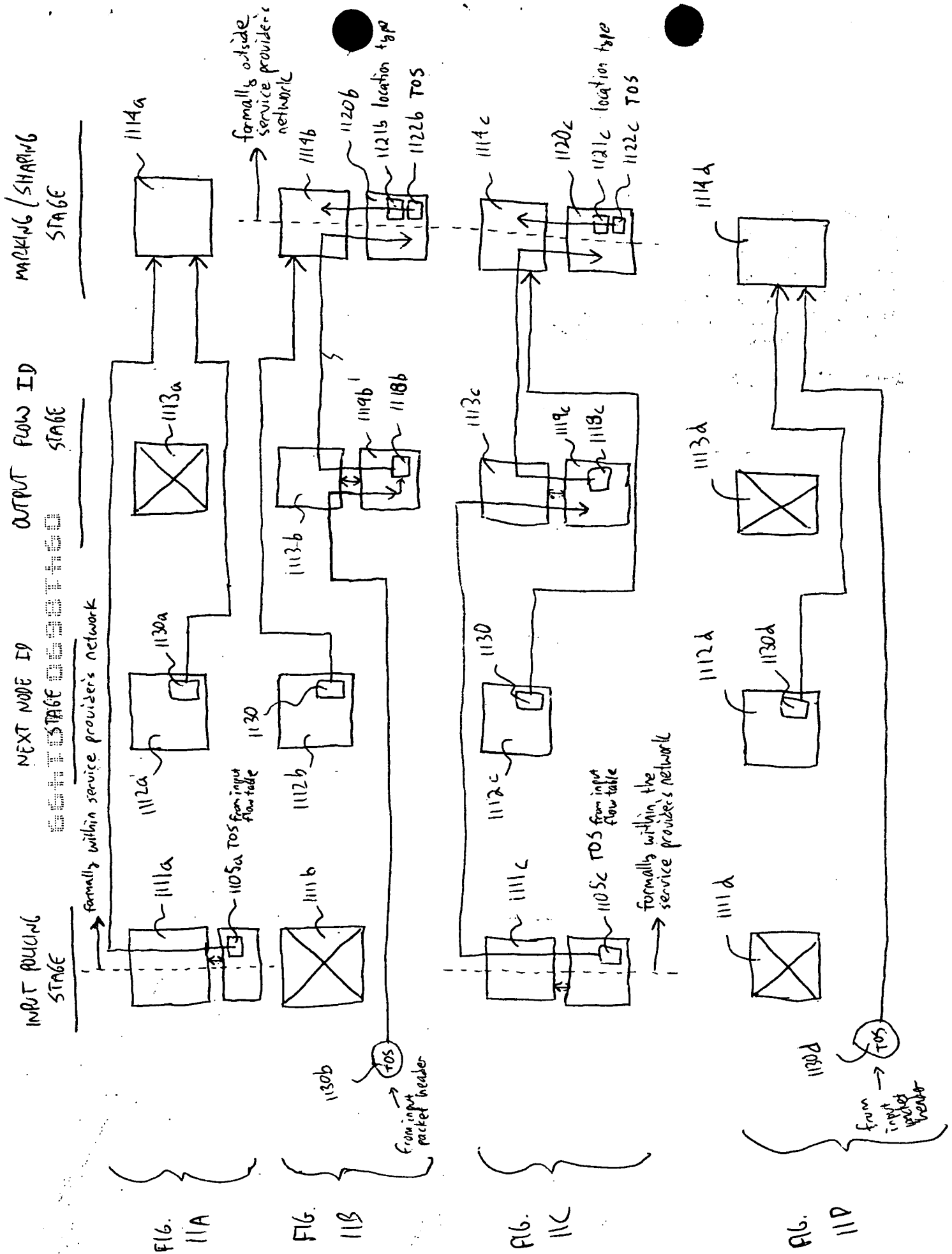
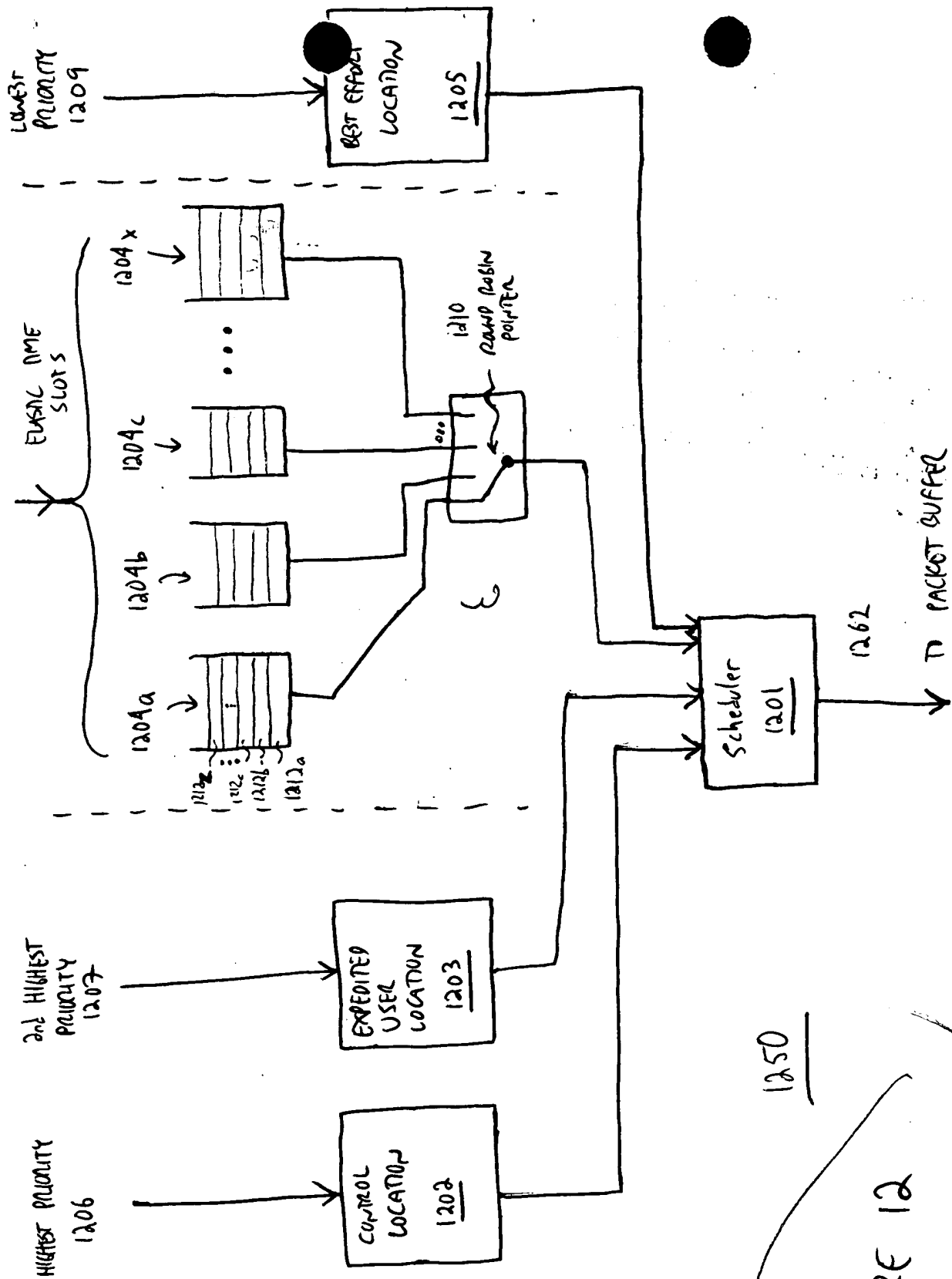


FIGURE 10



Setpoint = 0.6921450

3rd HIGHEST PRIORITY
1208



1250

FIGURE 12

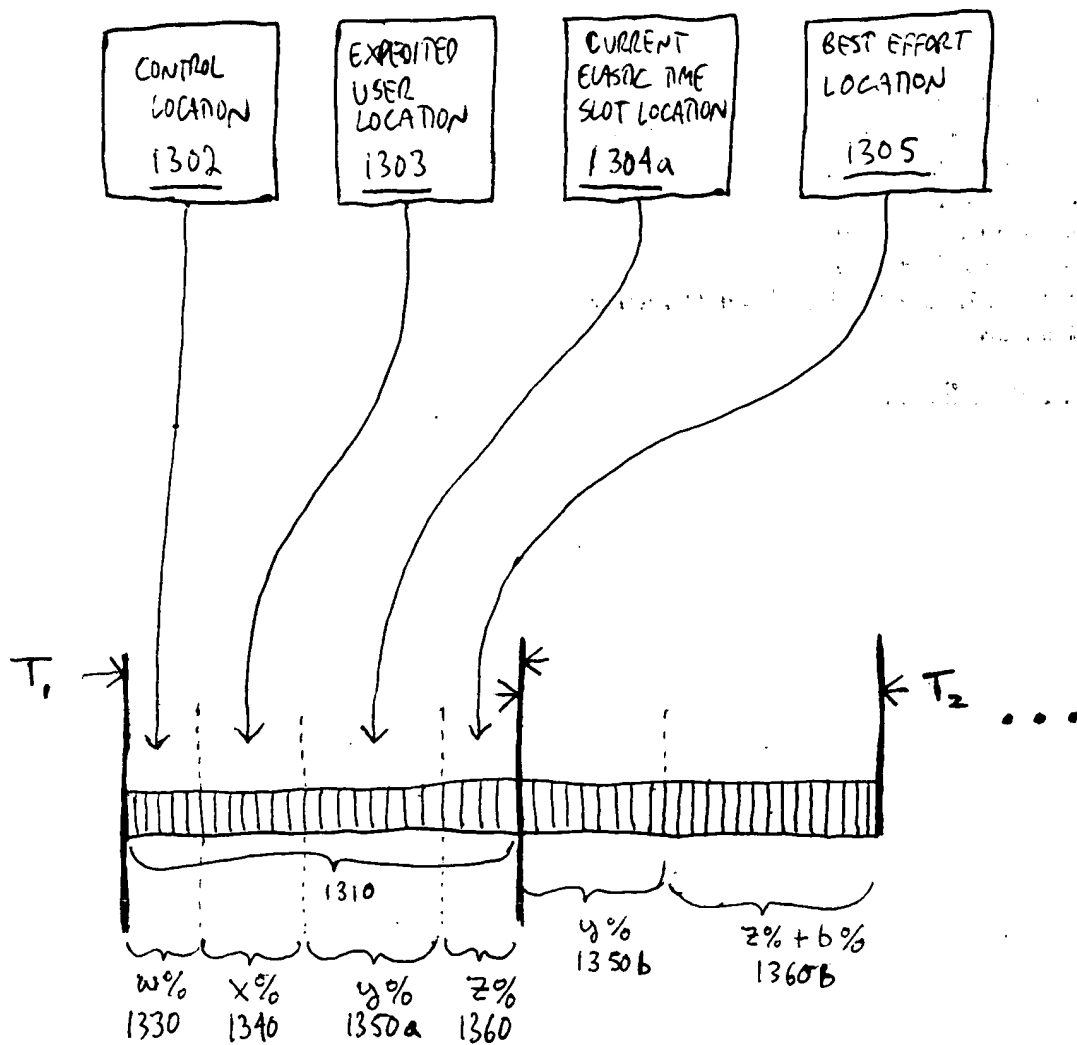


FIGURE 13

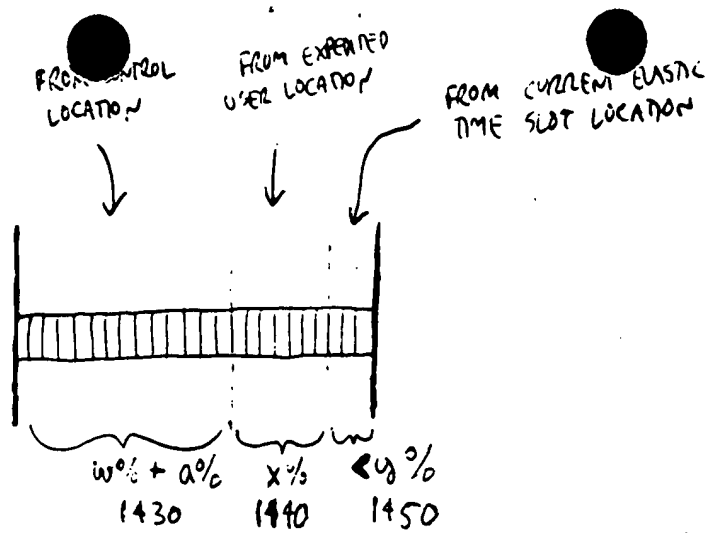


FIG. 14a

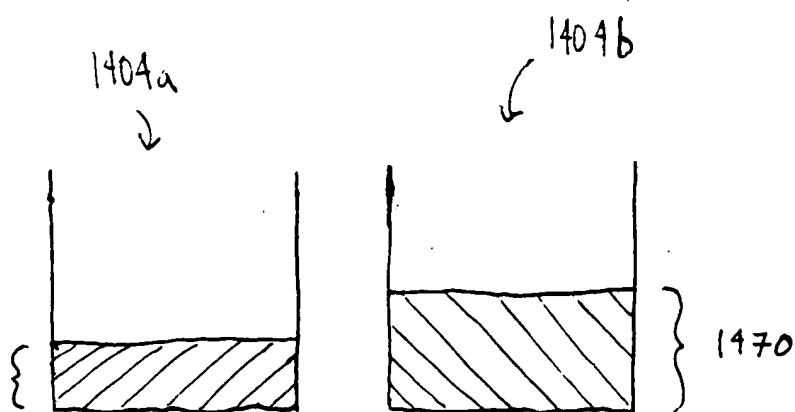


FIG. 14b

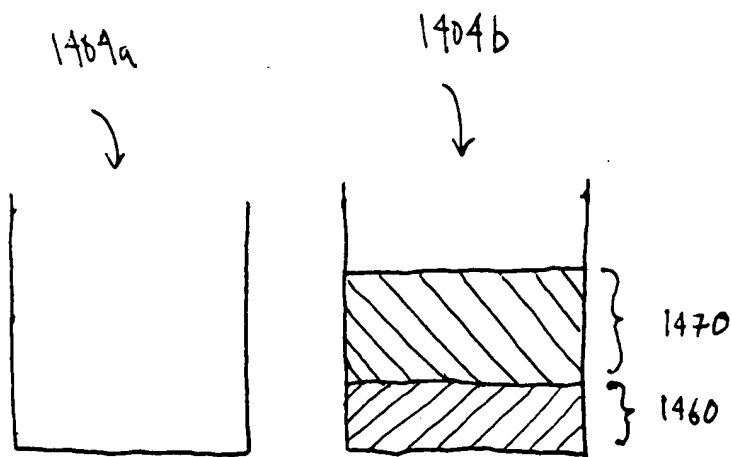


FIG. 14c

1460

1500

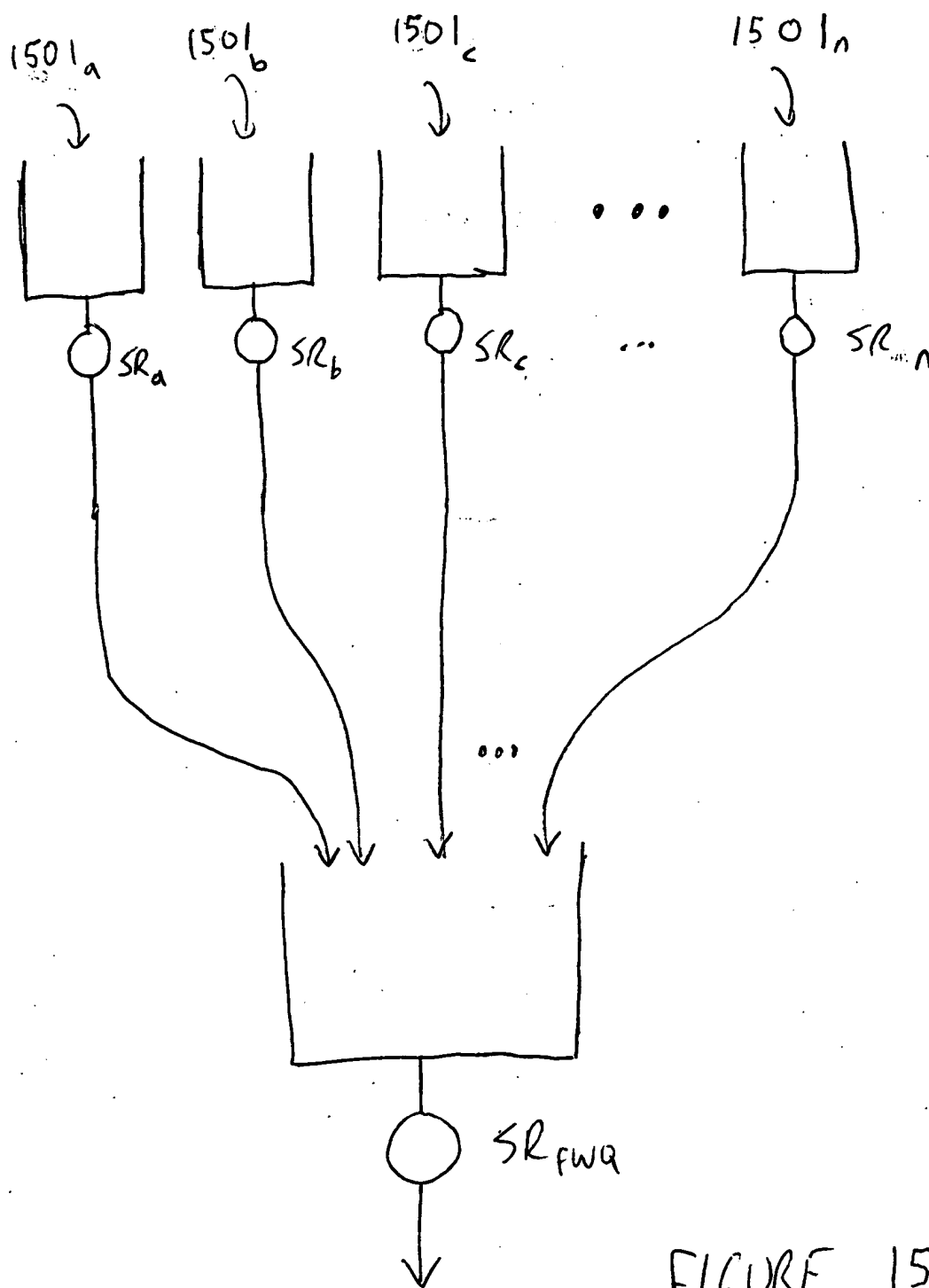


FIGURE 15

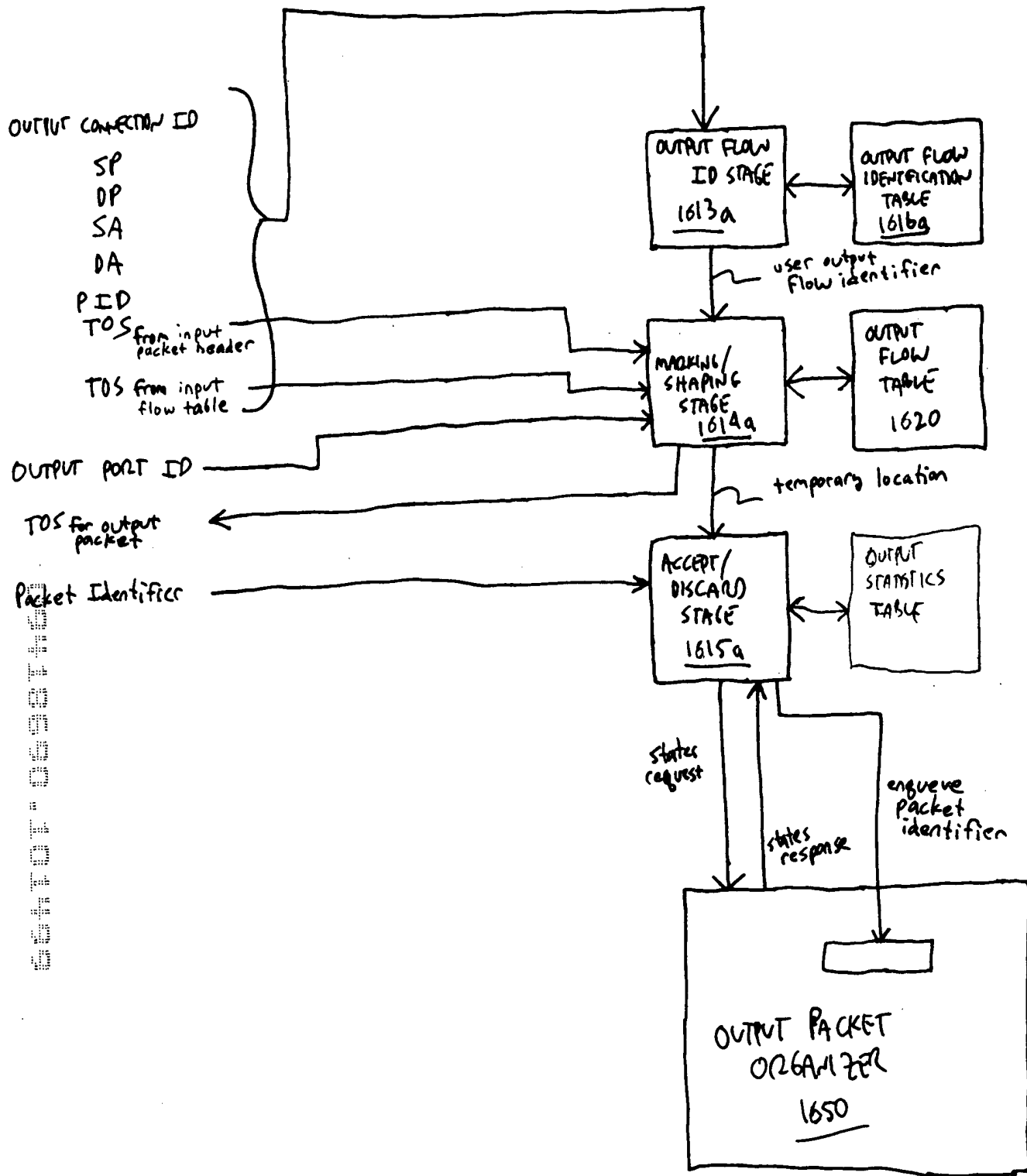


FIGURE 16_a

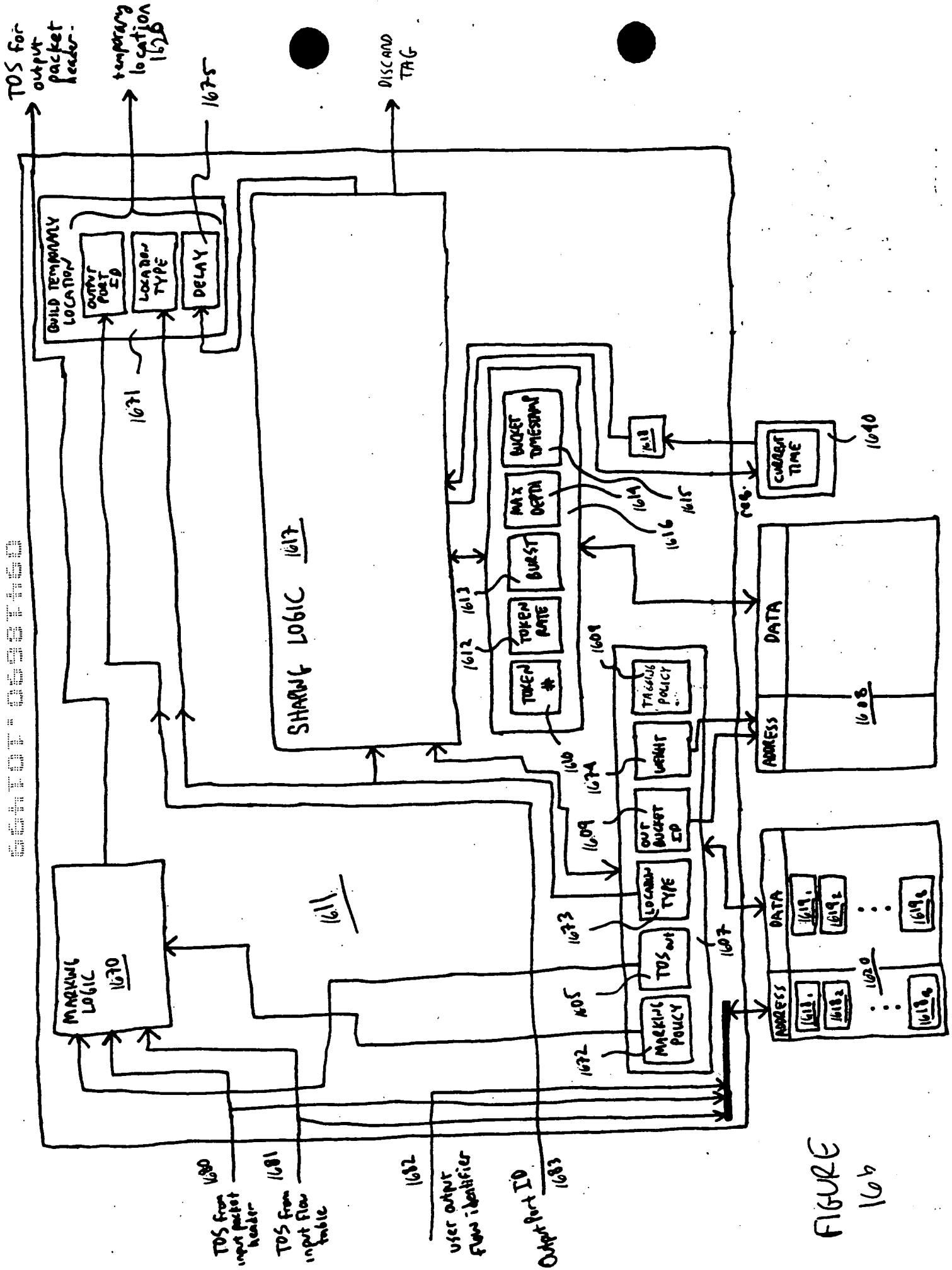


FIGURE 10b

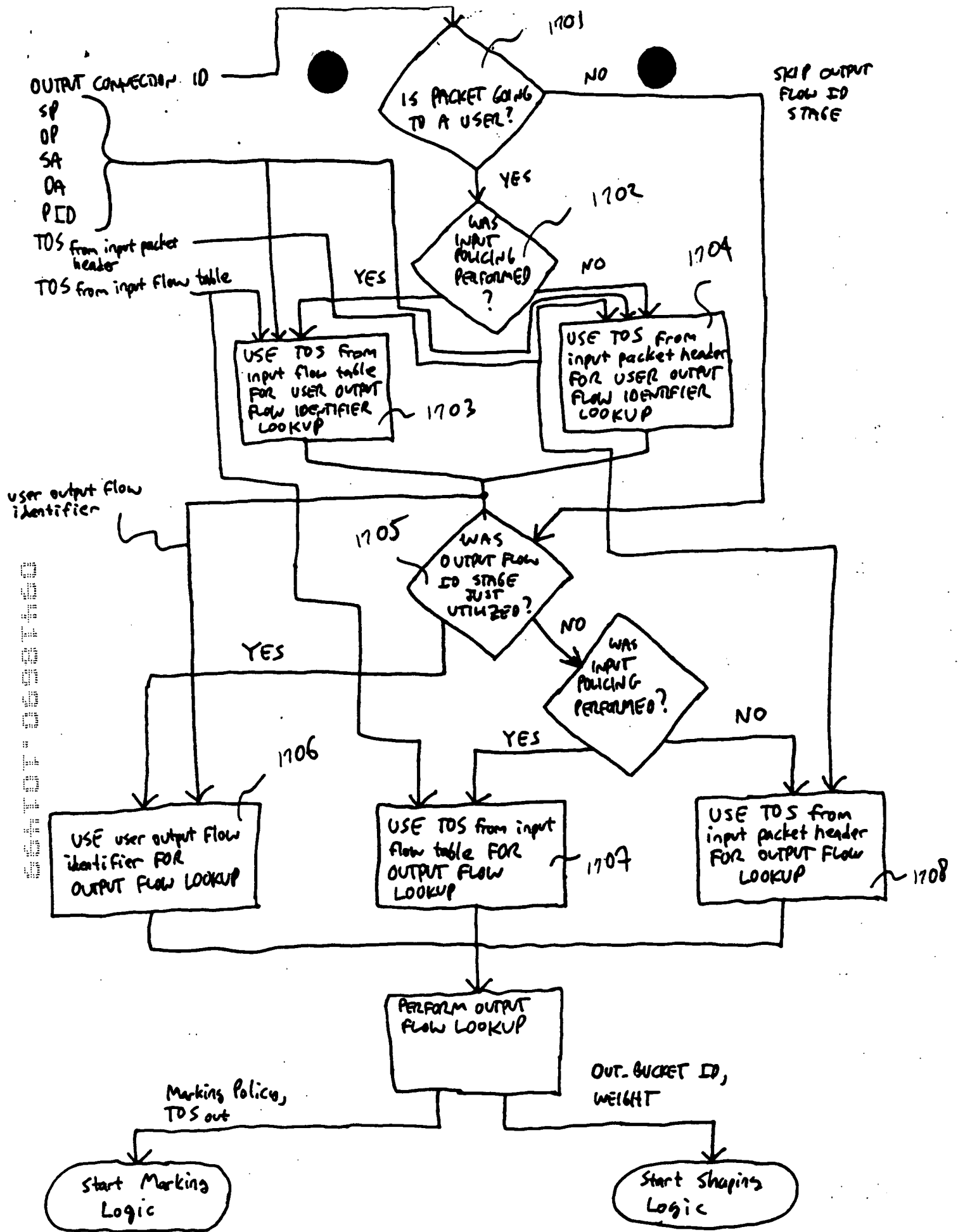


FIGURE 17a

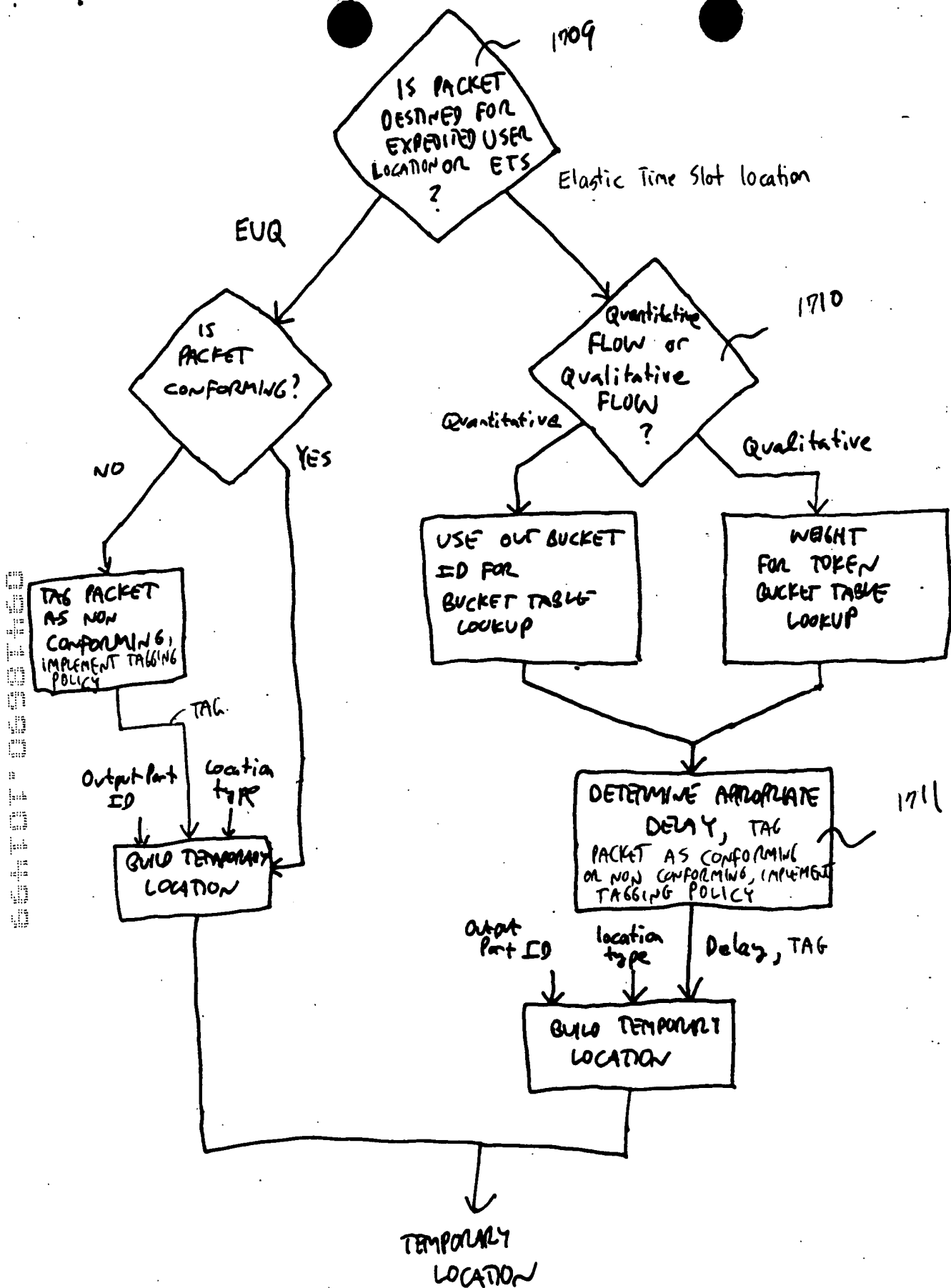


FIGURE 17b

Marking Logic

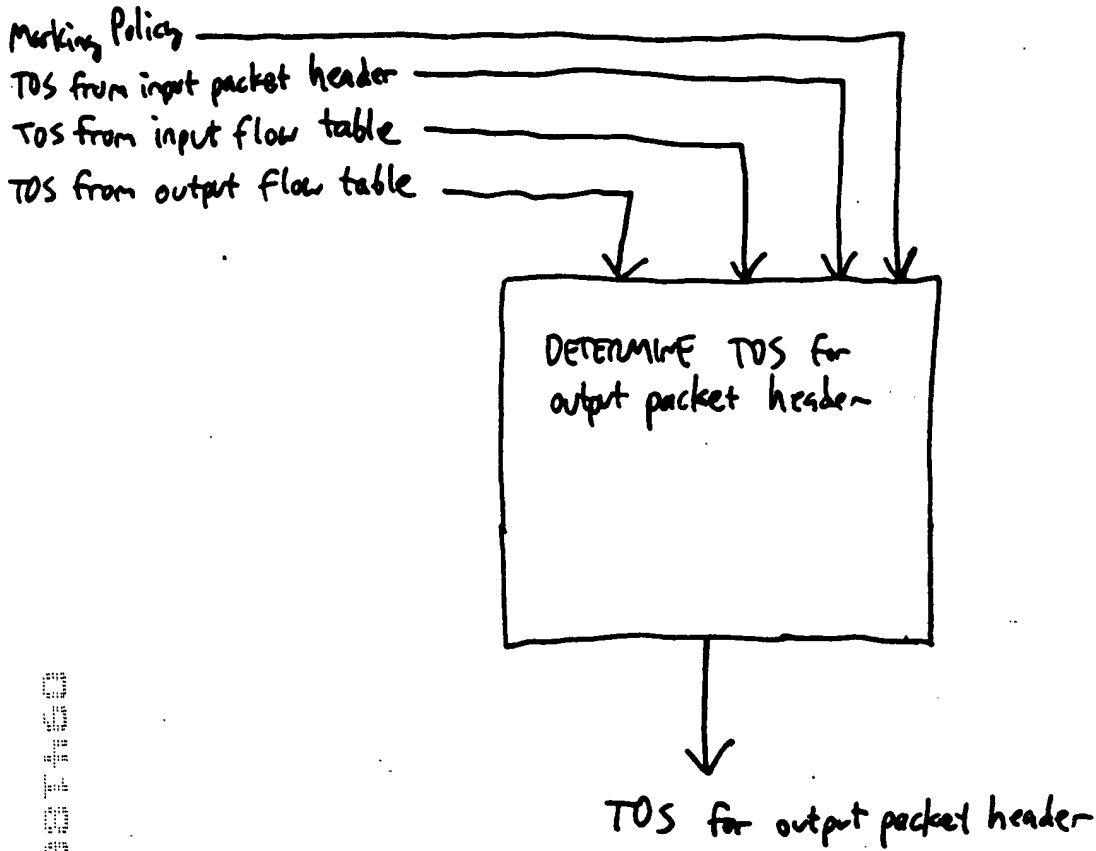


FIGURE 17c

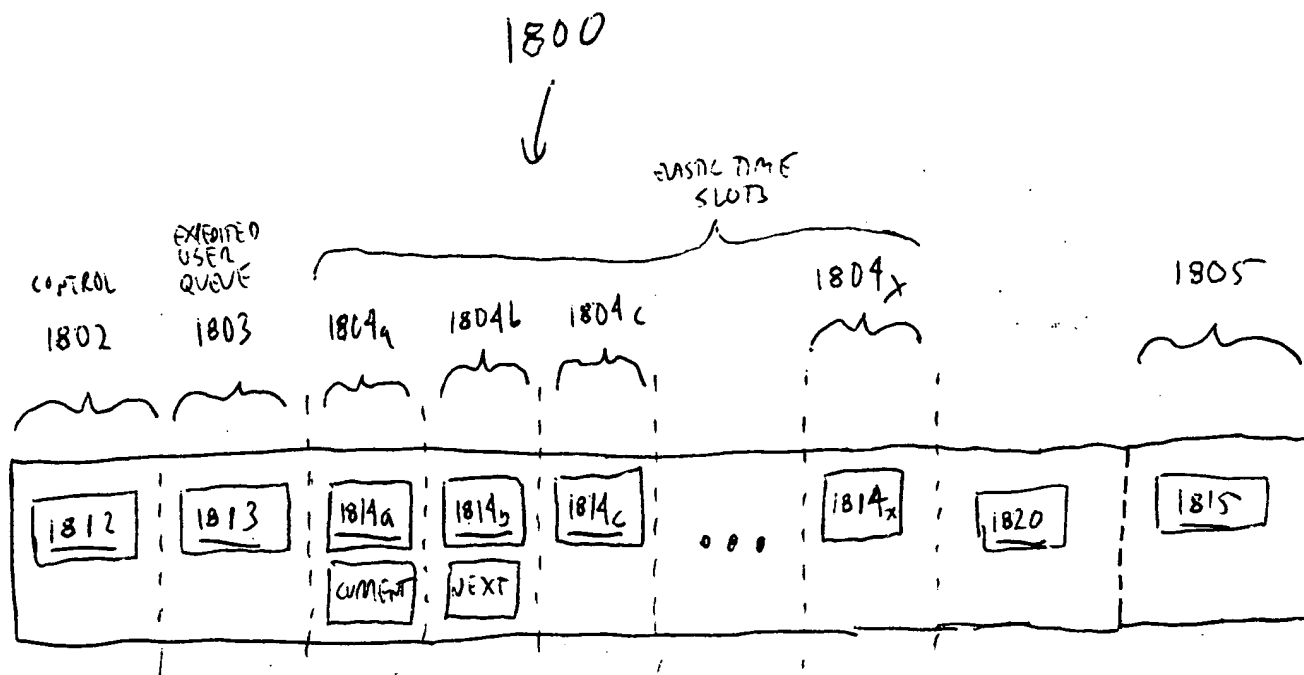


FIGURE 18

05440600-104400